

| | | | | |
|--|----------|--|--------|----------------------|
| Electronic Patent Application Fee Transmittal | | | | |
| Application Number: | | 10525999 | | |
| Filing Date: | | 28-Nov-2005 | | |
| Title of Invention: | | Method and device for verifying digital circuits | | |
| First Named Inventor/Applicant Name: | | Stefan Horeth | | |
| Filer: | | John Alfred Lingl/Janet Levoska | | |
| Attorney Docket Number: | | 12951-7 | | |
| Filed as Large Entity | | | | |
| U.S. National Stage under 35 USC 371 Filing Fees | | | | |
| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
| Basic Filing: | | | | |
| Pages: | | | | |
| Claims: | | | | |
| Miscellaneous-Filing: | | | | |
| Petition: | | | | |
| Patent-Appeals-and-Interference: | | | | |
| Post-Allowance-and-Post-Issuance: | | | | |
| Extension-of-Time: | | | | |
| Extension - 3 months with \$0 paid | 1253 | 1 | 1110 | 1110 |

| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
|-------------------|----------|----------|--------|----------------------|
| Miscellaneous: | | | | |
| Total in USD (\$) | | | | 1110 |